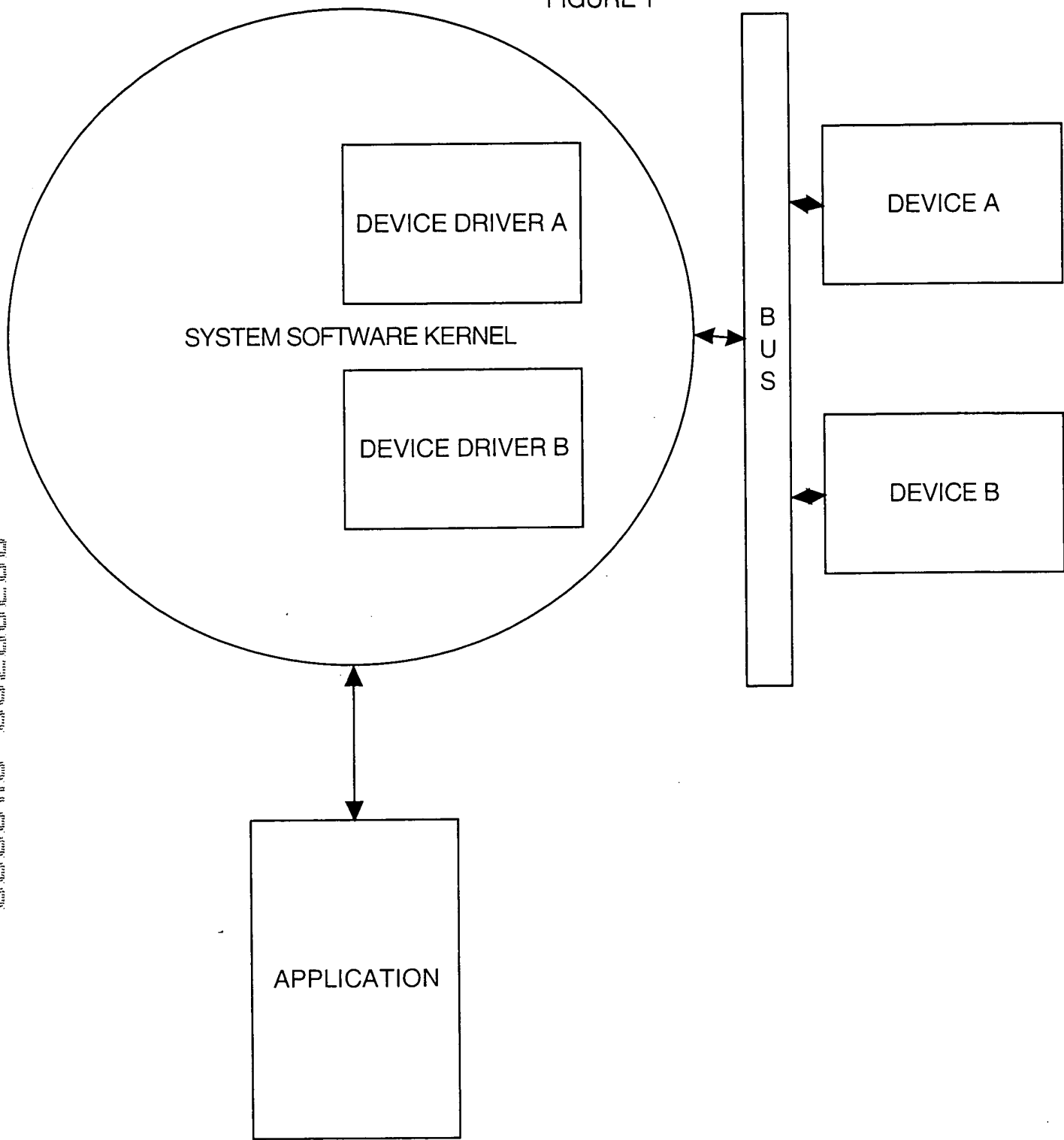


FIGURE 1



Device Management Architecture

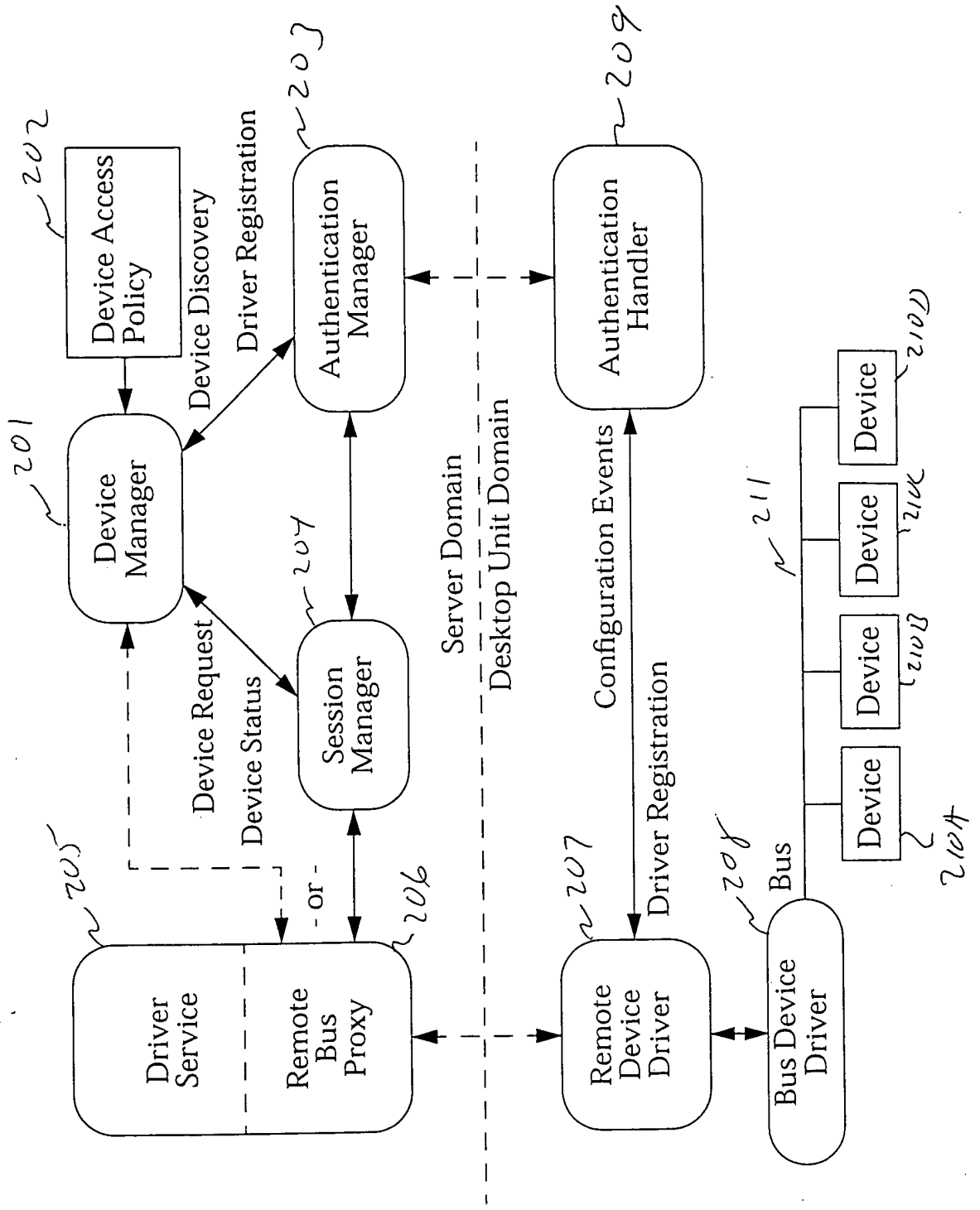


Figure 2

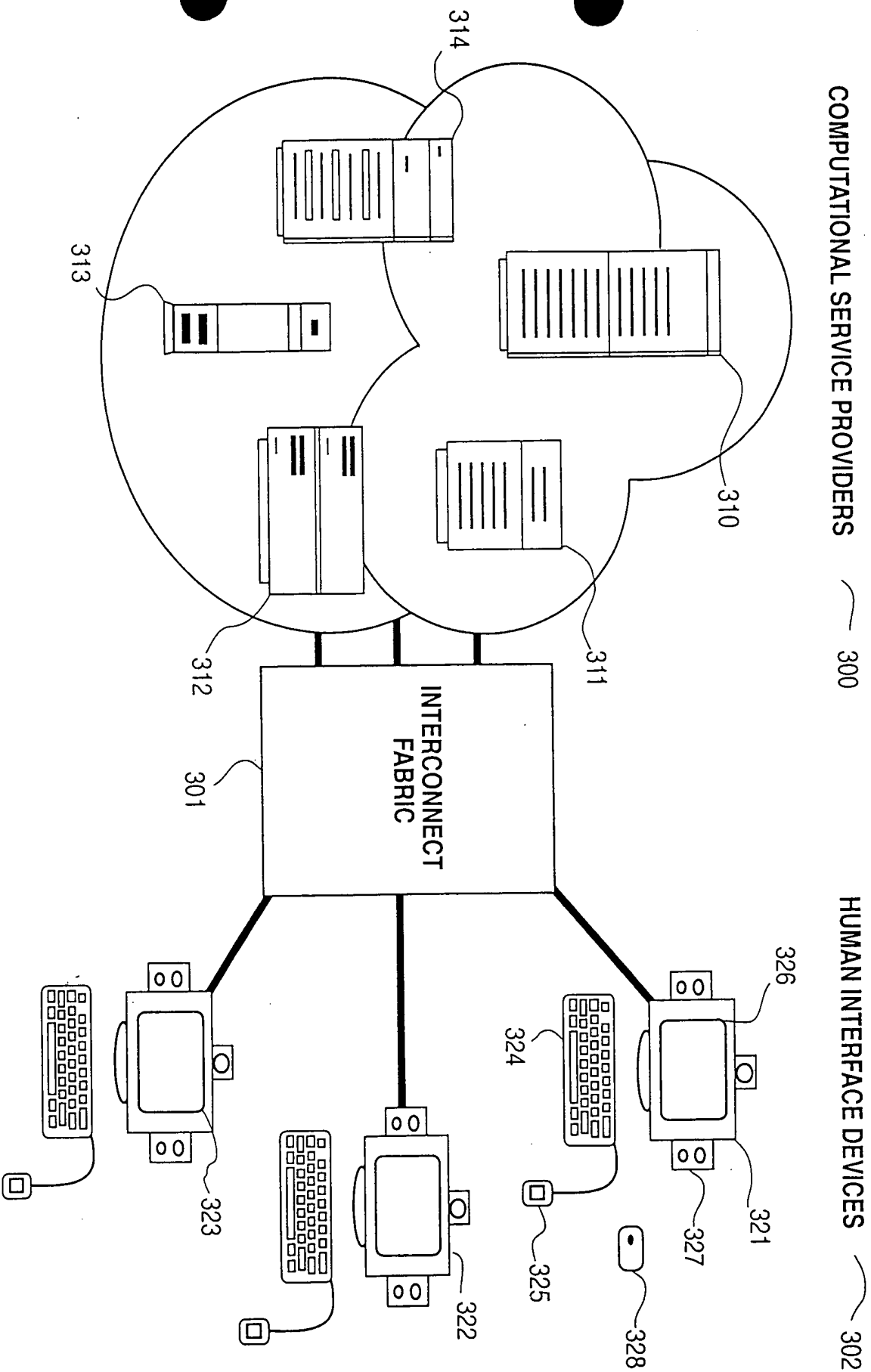


FIG. 3

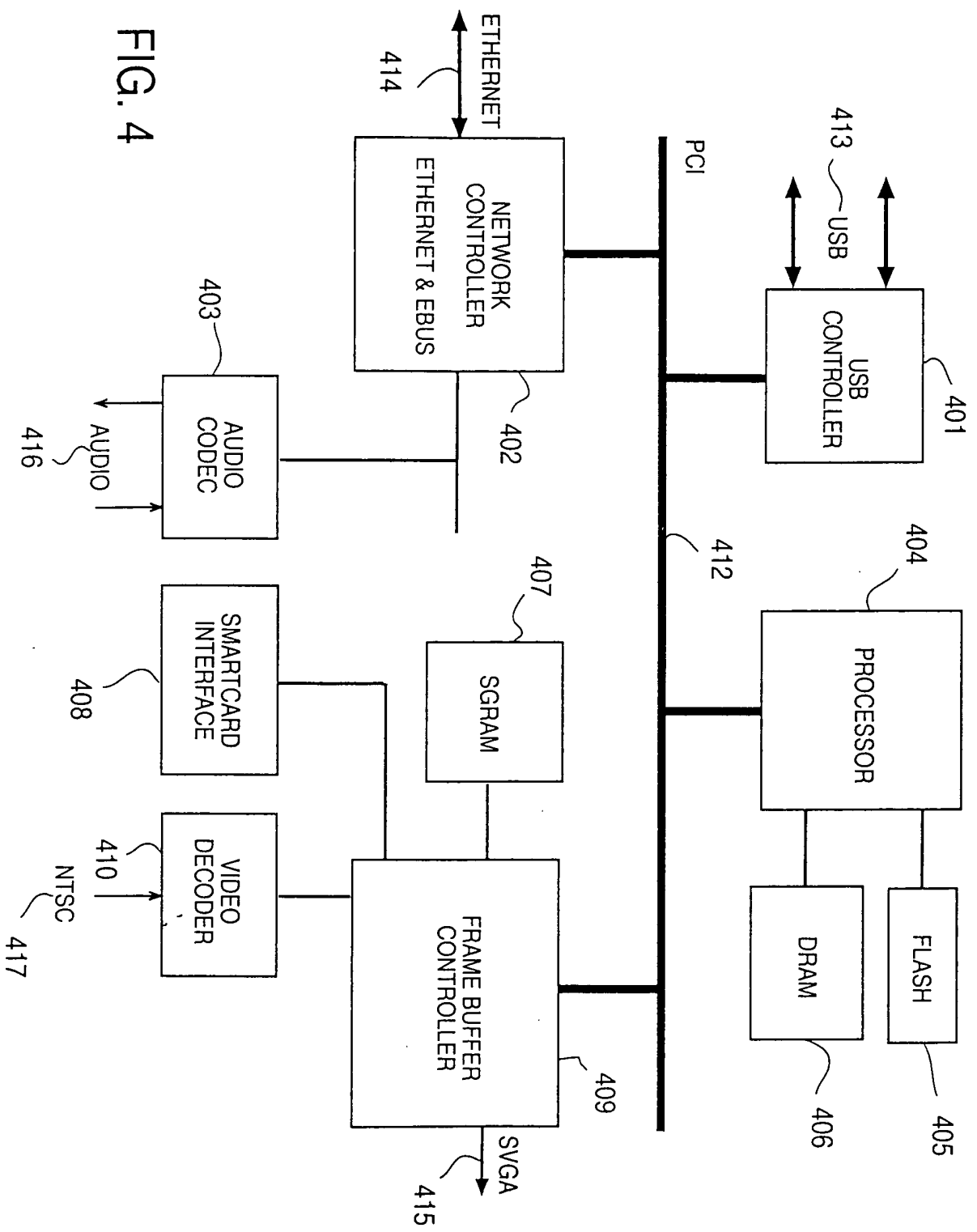


FIG. 4

092929 040929

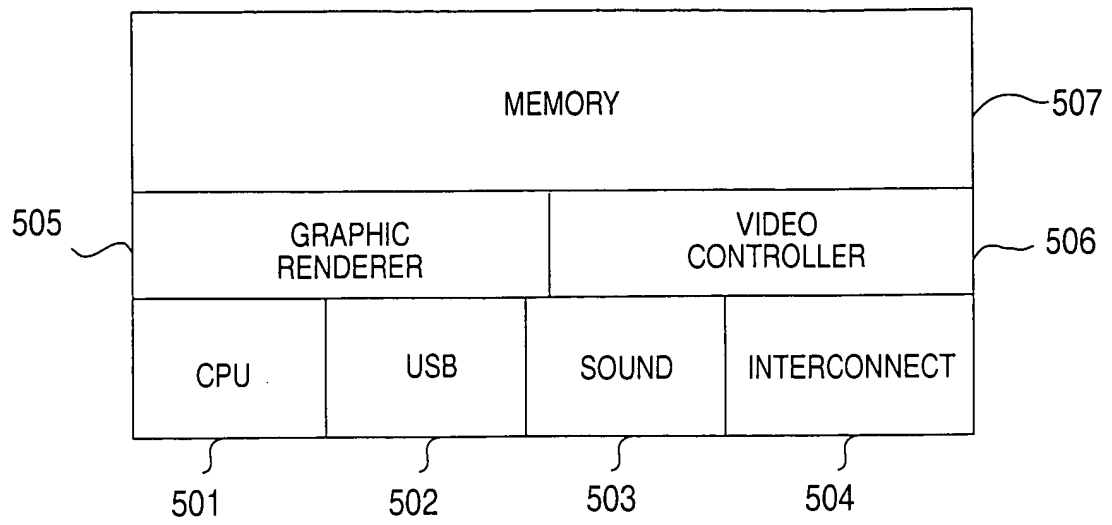


FIG. 5